

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: V. JAIRAZBHOY, et al.
Appln. No.: 09/920,192
Filed: August 1, 2001
For: METHOD FOR MAKING AN
ELECTRONIC CIRCUIT
ASSEMBLY
Attorney Docket No: 10541-609

Examiner: Minh N. Trinh
Art Unit: 3729

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REPLY UNDER 37 C.F.R. 1.116

In response to the Office Action mailed April 28, 2004 (Part of Paper No./Mail Date 20040226), Applicants respectfully submit this paper comprising the following parts:

- I. Listing of Claims, beginning on page 2;
- III. Remarks, beginning on page 5.

Applicants note that the Transmittal to which this paper is attached includes a Certificate of Facsimile Transmission under 37 C.F.R §1.8; and a fee statement calculating any fee(s) presently due in connection with the filing of this paper, along with an authorization to charge any fee deficiency to the deposit account of Applicant's assignee, Visteon Global Technologies, Inc., Deposit Account No. 06-1500.

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